

U.S. DEPARTMENT OF COMMERCE, PATENT AND TRADEMARK OFFICE		DATE: <b>September 20, 2001</b>
<b>TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371</b>		U.S. APPLN. NO. (if known): <b>09/926188</b>
INTERNATIONAL APPLICATION NO.: <b>PCT/JP00/01863</b>	INTERNATIONAL FILING DATE: <b>MARCH 27, 2000</b>	PRIORITY DATE CLAIMED: <b>MARCH 26, 1999</b>
TITLE OF INVENTION: <b>N-TYPE SEMICONDUCTOR DIAMOND AND METHOD OF ITS MAKING</b>		
APPLICANT(S) FOR DO/EO/US: <b>Toshihiro ANDO, Yoichiro SATO, Eiji YASU, Mika GAMO and Isao SAKAGUCHI</b>		
Applicant hereby submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:		
<p>1. <u>XX</u> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.</p> <p>2. <u>    </u> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371.</p> <p>3. <u>XX</u> This express request to begin national examination procedures (35 USC 371(f)) at any time rather than delay examination until the expiration of the time limit set in 35 USC 371(b) and PCT Articles 22 and 39(1).</p> <p>4. <u>XX</u> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.</p> <p>5. <u>XX</u> A copy of the International Application as filed (35 U.S.C. 371(c)(2)):</p> <p style="margin-left: 20px;">a. <u>    </u> is transmitted herewith (required only if not transmitted by the International Bureau).</p> <p style="margin-left: 20px;">b. <u>XX</u> has been transmitted by the International Bureau.</p> <p style="margin-left: 20px;">c. <u>    </u> is not required, as the application was filed in the United States Receiving Office (RO/US)</p> <p>6. <u>XX</u> A translation of the International Application into English (35 U.S.C. 371(c)(2)).</p> <p>7. <u>XX</u> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))</p> <p style="margin-left: 20px;">a. <u>    </u> are transmitted herewith (required only if not transmitted by the International Bureau).</p> <p style="margin-left: 20px;">b. <u>    </u> have been transmitted by the International Bureau.</p> <p style="margin-left: 20px;">c. <u>    </u> have not been made; however, the time limit for making such amendments has NOT expired.</p> <p style="margin-left: 20px;">d. <u>XX</u> have not been made and will not be made.</p> <p>8. <u>    </u> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</p> <p>9. <u>XX</u> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).</p> <p>10. <u>XX</u> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).</p>		
<b>ITEMS 11. TO 16. BELOW CONCERN OTHER DOCUMENT(S) OR INFORMATION INCLUDED:</b>		
<p>11. <u>    </u> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</p> <p>12. <u>XX</u> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.  ASSIGNEES NAMES AND ADDRESSES (1) <b>JAPAN SCIENCE AND TECHNOLOGY CORPORATION, Kawaguchi-shi, Japan; and</b>  (2) <b>NATIONAL INSTITUTE FOR MATERIALS SCIENCE, Tsukuba-shi, Japan</b>  <b>Please publish the assignee data with the application.</b></p> <p>13. <u>XX</u> A <b>FIRST</b> preliminary amendment.  <u>    </u> A <b>SECOND</b> or <b>SUBSEQUENT</b> preliminary amendment</p> <p>14. <u>    </u> A substitute specification.</p> <p>15. <u>    </u> A change of power of attorney and/or address letter.</p> <p>16. <u>XX</u> Other items or information: Petition for Acceptance of Color Photographs w/check for \$130.00; 13 sheets of drawings and International search report.</p>		

U.S. APPLICATION NO. (if known) <b>09/926188</b>	INTERNATIONAL APPLICATION NO. <b>PCT/JP00/01863</b>	DATE: <b>September 20, 2001</b>
---	--	---------------------------------

17. <input checked="" type="checkbox"/> The following fees are submitted:  <b>Basic National Fee (37 CFR 1.492(a)(1)-(5):</b> Search Report has been prepared by the EPO or JPO: ..... \$860.00  International preliminary examination fee paid to USPTO (37 CFR 1.482) ..... \$690.00  No international preliminary examination fee paid to USPTO (37 CFR 1.482) but international search fee paid to USPTO (37 CFR 1.445(a)(2)) ..... \$710.00  Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO ..... \$1000.00  International preliminary examination fee (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(2)-(4) ..... \$100.00  <div style="text-align: right;"><b>ENTER APPROPRIATE BASIC FEE AMOUNT = \$ 860.00</b></div>	<b>CALCULATIONS</b>	<b>PTO USE ONLY</b>
---	---------------------	---------------------

Surcharge of <b>\$130.00</b> for furnishing the oath or declaration later than __ 20 30 months from the earliest claimed priority date (37 CFR 1.492(e)).		
--	--	--

CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
TOTAL	7 - 20 =		X \$ 18.00		
INDEPENDENT	2 - 3 =		X \$ 80.00		
Multiple dependent claims(s) (if applicable)			+ \$270.00		
<b>TOTAL OF ABOVE CALCULATIONS =</b>				<b>\$ 860.00</b>	
Reduction by 1/2 for filing by small entity, if applicable. (Note 37 CFR 1.9, 1.27, 1.28).					
<b>SUBTOTAL =</b>				<b>\$ 860.00</b>	
Processing fee of <b>\$130.00</b> for furnishing the English translation later than __ 20 30 months from the earliest claimed priority date (37 CFR 1.492(f)). +					
<b>TOTAL NATIONAL FEE =</b>				<b>\$ 860.00</b>	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). <div style="text-align: right;"><b>\$40.00 per property +</b></div>				<b>\$ 40.00</b>	
<b>TOTAL FEES ENCLOSED =</b>				<b>\$ 900.00</b>	
				Amount to be: <div style="text-align: right;">_____ refunded \$ _____</div> <div style="text-align: right;">_____ charged \$ _____</div>	

ATTORNEY'S DOCKET NO: 011147

U.S. APPLICATION NO. (if known) <b>09/926188</b>	INTERNATIONAL APPLICATION NO.  <b>PCT/JP00/01863</b>	DATE: <b>September 20, 2001</b>
--	--	---------------------------------


a. **XX** A check in the amount of \$900.00 to cover the above fees is enclosed. (\$860.00 for basic filing fee and \$40.00 for assignment recordation fee). (This paper is filed in triplicate)


b.      Please charge my Deposit Account No. 01-2340 in the amount of \$      to cover the above fees. (A duplicate copy of this sheet is enclosed.)

c. **X** The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 01-2340.

**NOTE:** Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed to request that the application be restored to pending status.

**Send All Correspondence To:**

  
**23850**  
 PATENT TRADEMARK OFFICE

  
 SIGNATURE  
Stephen G. Adrian  
 NAME  
32,878  
 REGISTRATION NUMBER

ARMSTRONG, WESTERMAN, HATTORI,  
 McLELAND & NAUGHTON, LLP  
 Suite 1000, 1725 K Street, N.W.  
 Washington, D. C. 20006  
 Tel: (202) 659-2930  
 Fax: (202) 887-0357

JC03 Rec'd PCT/TO 20 SEP 2001

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Toshihiro ANDO et al.**Serial Number: **Not Yet Assigned**  
(PCT/JP00/01863)Filed: **September 20, 2001**For: **N-TYPE SEMICONDUCTOR DIAMOND AND METHOD OF ITS MAKING**PRELIMINARY AMENDMENT AND SUBMISSION OF  
SUBSTITUTE PAGES FOR PCT ARTICLE 34 AMENDMENTCommissioner for Patents  
Washington, D.C. 20231

September 20, 2001

Sir:

Prior to the calculation of the filing fees of the above application, please amend the application as follows:

IN THE SPECIFICATION:

Please enter the attached substitute sheets for pages 4-7. The substitute sheets incorporate into the English language translation of the international application amendments presented in the international stage under PCT Article 34.

On substitute page 7, between line 5 and 6 and after the subtitle of Brief Description of the Drawings insert the following paragraph:

--This application contains at least one drawing executed in color. Copies of this patent or patent application publication with color drawing(s) will be provided by the Office upon request and payment of the necessary fee.--

IN THE CLAIMS:

Please cancel claims 8-19 without prejudice and disclaimer.

Please amend claims 1-7 as follows:

1. (Amended) An n-type semiconductor diamond, characterized by:

a crystalline perfectness whereby:

it has impurity atoms constituted by sulfur atoms forming a single donor level of 0.38 eV,

it has a carrier mobility temperature dependency which at a temperature (T) range in excess of the room temperature is  $T^{-3/2}$  dependent, and

it has a diamond peak in its Raman spectrum, whose half width is  $2.6 \text{ cm}^{-1}$ ;

a crystalline perfectness whereby:

light emission by excitons is observable; and

a crystalline perfectness whereby:

a distinct Kikuchi pattern in its reflection electron diffraction analysis is observable.

2. (Amended) An n-type semiconductor diamond as set forth in claim 1, characterized in that

at a room temperature it has a carrier concentration not less than  $1.4 \times 10^{13} \text{ cm}^{-3}$  and

a carrier mobility not less than  $580 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$ .

3. (Amended) A method of making an n-type semiconductor diamond, characterized in that it comprises:

mechanically polishing a diamond substrate to make it an inclined diamond substrate;

subjecting a surface of said inclined diamond substrate to a smoothening treatment make it even;

exciting a raw material gas made of a volatile hydrocarbon compound, a sulfur compound and a hydrogen gas by a microwave plasma while maintaining at a given temperature said substrate surface smoothened as aforesaid to cause n-type semiconductor diamond to grow epitaxially on said surface smoothened substrate.

4. (Amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said diamond substrate is a diamond (100) face oriented substrate.

5. (Amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said inclined substrate is formed to consist of steps each in the order of an atomic layer, by mechanically polishing a diamond (100) face oriented substrate so that its face normal is inclined at an angle in a range between 1.5 degree and 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made by either its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions.

6. (Amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said hydrogen plasma exposure treatment comprises a treatment of exposing said inclined substrate to the hydrogen plasma of a hydrogen pressure of 10 to 50 Torr and a microwave output of 200 to 1200 W at a substrate temperature of 700 to 1200 for a period of 0.5 hour to 5 hours, thereby to make even said substrate surface in the order of an atomic layer.

7. (Amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said given substrate temperature lies in a range between 700 and 1100°C, preferably at 830°C.

REMARKS


The above amendment is submitted to place the specification and claims in substantially the same condition as to the claims which have been amended under Article 34 in the international application. An English translation of the annexes of the PCT international preliminary examination report is enclosed. Early and favorable action is awaited.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "Version with markings to show changes made."

In the event there are any additional fees required, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI,  
McLELAND & NAUGHTON, LLP

  
Stephen G. Adrian  
Reg. No. 32,878

Atty. Docket No. 011147  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
Tel: (202) 659-2930  
SGA/yap

Enclosures: Translation of the Annexes of the PCT International Preliminary Examination Report with Substitute pages 4-7.

VERSION WITH MARKINGS TO SHOW CHANGES MADE

Claims 1-7 have been amended as follows:

1. (Amended) ~~A method of making An n-type semiconductor diamond, characterized in that it comprises by:~~

~~mechanically polishing a diamond substrate to make it an inclined diamond substrate;~~

~~subjecting a surface of said inclined diamond substrate to a smoothening treatment to make the surface even;~~

~~exciting a raw material gas made of a volatile hydrocarbon compound, a sulfur compound and a hydrogen gas by a microwave plasma while maintaining at a given temperature said diamond substrate surface smoothened as aforesaid to cause n-type semiconductor diamond to grow epitaxially on said surface smoothened substrate.~~

a crystalline perfectness whereby:

it has impurity atoms constituted by sulfur atoms forming a single donor level of 0.38 eV,

it has a carrier mobility temperature dependency which at a temperature (T) range in excess of the room temperature is  $T^{-3/2}$  dependent, and

it has a diamond peak in its Raman spectrum, whose half width is  $2.6 \text{ cm}^{-1}$ ;

a crystalline perfectness whereby:

light emission by excitons is observable; and

a crystalline perfectness whereby:

a distinct Kikuchi pattern in its reflection electron diffraction analysis is observable.

2. (Amended) ~~A method of making~~ An n-type semiconductor diamond as set forth in claim 1, characterized in that ~~said diamond substrate comprises a diamond (100) face oriented substrate~~ at a room temperature it has a carrier concentration not less than  $1.4 \times 10^{-13} \text{ cm}^{-3}$  and a carrier mobility not less than  $580 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ .

3. (Amended) A method of making an n-type semiconductor diamond ~~as set forth in claim~~ ~~±~~, characterized in that ~~said inclined substrate is formed by mechanically polishing a diamond (100) face oriented substrate surface so that its face normal is inclined at an angle in a range between 1.5 degree and 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made by either its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions~~ it comprises:

mechanically polishing a diamond substrate to make it an inclined diamond substrate;

subjecting a surface of said inclined diamond substrate to a smoothening treatment make it even;

exciting a raw material gas made of a volatile hydrocarbon compound, a sulfur compound and a hydrogen gas by a microwave plasma while maintaining at a given temperature said substrate surface smoothened as aforesaid to cause n-type semiconductor diamond to grow epitaxially on said surface smoothened substrate.

4. (Amended) A method of making an n-type semiconductor diamond as set forth in claim ~~±~~ 3, characterized in that ~~said smoothening treatment comprises either a treatment in which said inclined substrate is exposed to a hydrogen plasma or a treatment in which it is exposed to an oxidizing flame such as an acetylene combustion flame~~ diamond substrate is a diamond (100) face oriented substrate.

5. (Amended) A method of making an n-type semiconductor diamond as set forth in claim ~~± 3~~, characterized in that said ~~hydrogen plasma exposure treatment comprises a treatment of exposing said inclined substrate to the hydrogen plasma of a hydrogen pressure of 10 to 50 Torr and a microwave output of 200 to 1200 W at a substrate temperature of 700 to 1200 for a period of 0.5 hour to 5 hours~~ inclined substrate is formed to consist of steps each in the order of an atomic layer, by mechanically polishing a diamond (100) face oriented substrate so that its face normal is inclined at an angle in a range between 1.5 degree and 6 degrees with respect to its <100> direction in a plane made by either its <100> and <010> directions or its <100> and <001> directions.

6. (Amended) A method of making an n-type semiconductor diamond as set forth in claim ~~± 3~~, characterized in that said ~~given substrate temperature lies in a range between 700 and 1100; preferably at 830~~ hydrogen plasma exposure treatment comprises a treatment of exposing said inclined substrate to the hydrogen plasma of a hydrogen pressure of 10 to 50 Torr and a microwave output of 200 to 1200 W at a substrate temperature of 700 to 1200 for a period of 0.5 hour to 5 hours, thereby to make even said substrate surface in the order of an atomic layer.

7. (Amended) A method of making an n-type semiconductor diamond as set forth in claim ~~± 3~~, characterized in that said ~~volatile hydrocarbon compound is an alkane or an alkene~~ given substrate temperature lies in a range between 700 and 1100°C, preferably at 830°C.

SPECIFICATIONN-TYPE SEMICONDUCTOR DIAMOND  
AND METHOD OF ITS MAKING

5

Technical Field

10 This invention relates to an n-type semiconductor diamond that can be used in electronic devices such as a small, high power device, a high output, high frequency device and a radiation-resistant integrated circuit which has so far been impossible to realize with a conventional semiconductor, and more particularly to such a diamond with n-type conductivity and yet having high mobility and good crystallinity, as well as a method of its making. In particular, the invention relates to an n-type semiconductor diamond formed by  
15 adding donor atoms effectively to diamond and to a method of making the same.

Background Art

20 Si semiconductor and gallium arsenide semiconductor are currently in use with the problem that micronizing and densifying these devices gives rise to a rise in electric field strength in their interior and heat buildup is encountered in their use. Henceforth these semiconductors will have to adapt themselves to meet severe environments.

25 In contrast, diamond is a wide band gap semiconductor, is the highest in the mobility of electrons and positive holes, is extremely high in breakdown electric field and yet is the least to produce electron and positive hole pairs at an elevated temperature or as influenced by a radiation. Diamond semiconductors, thus with the  
30 ability by nature to meet severe environments, are usable in high power, high frequency operating and high temperature operating devices. To realize such a diamond semiconductor device, thin films

of diamond crystal of good quality are required.

So far, it has been possible to fabricate low resistance p-type semiconductor diamond by boron dopin with ease. As for low resistance n-type semiconductor diamond, however, it has practically  
5 been difficult to obtain a thin film of semiconductor diamond crystal of satisfactory quality, despite the studies that have been pursued to investigate into a large number of manufacturing processes including doping into CVD diamond.

For example, it has been reported that doping diamond with  
10 nitrogen reduces its activation energy and makes it an insulator at a room temperature (Mat. Res. Soc. Symp. Proc. 162, 3 - 14, 1990). Further, a thin film of n-type diamond crystal doped with phosphorus has been reported, which is too high in electrical resistivity, however, to the extent that it is unsuitable for practical use (Mat. Res. Soc.  
15 Symp. Proc. 162, 23 - 34, 1990).

Also, an experiment to obtain an n-type diamond thin film from methane and hydrogen sulfide by a microwave plasma CVD technique has been reported (JP P 63-302516 A). However, as is apparent from Tables 1 and 2 of this patent literature, the n-type semiconductor  
20 diamond thin film made using the microwave plasma CVD process has an electron mobility that is abnormally high compared with that of an n-type semiconductor diamond crystal made by an extra-high pressure process shown in Table 2 in spite of the fact that both have an equivalent sulfur concentration. That is to say, it is shown that  
25 this n-type semiconductor diamond thin film is deficient and not applicable in a semiconductor electronic device.

Further, in the fabrication of phosphorus doped diamond by the microwave plasma CVD technique, there have been known a phosphorus doping process that introduces phosphine into a reaction  
30 gas of hydrogen and hydrocarbon and decomposes phosphine in a microwave plasma, and another phosphorus doping process that decomposes phosphine at a high temperature or by irradiation with an

ultraviolet ray.

However, the preceding microwave plasma CVD process because it dopes diamond with phosphorus in a state that it is combined with hydrogen will not make phosphorus a source of supply  
5 of electrons and, if phosphorus is doped, only yields n-type semiconductor diamond that is low in carrier mobility and deep in level. For these reasons, no n-type semiconductor of this type that is of the quality which makes it applicable to a semiconductor electronic device has been obtained.

10 Therefore, as far as the CVD technique is concerned, while thus far there have already been reports about methods tried to make a thin film of n-type semiconductor diamond as mentioned above, none has as yet been obtained thereby that is of a quality level enough to justify its use in a semiconductor electronic device.

15 Besides, a method has also been known that implants diamond with phosphorus ions accelerated. According to this method, phosphorus that is greater in mass than carbon is implanted, which creates a defect in diamond. Further, because phosphorus without combining with carbon is interstitially included in the diamond lattice,  
20 it is more than hard to create such a combination therein. Thus, any such n-type semiconductor of acceptable quality has not as yet been obtained by this method either.

Further, there has also been known the chemical transport reaction process that dopes diamond with phosphorus, which places  
25 graphite and red phosphorous in the reaction system and evaporates them in the system to synthesize diamond while doping it with phosphorus.

However, a difference in the rate of reaction and the rate of evaporation between graphite and red phosphorous makes it hard for  
30 the process to control the phosphorus concentration. Indeed, no such n-type semiconductor of satisfactory quality sought has been obtained by this process either.

09/926188

JC03 Rec'd PCT/PTO 20 SEP 2001

Also, there has recently been proposed an n-type diamond semiconductor having atoms of a valence number of 5 or more added thereto as donor atoms. See JP P 10-194889 A. However, a phosphorus added n-type semiconductor diamond applicable to a semiconductor electronic device has remained unrealized and a method of its making has become a problem awaiting solution.

This invention is aimed to solve such problems encountered in the prior art and has for its first object to provide an n-type semiconductor diamond having a perfect crystallinity that is applicable to a semiconductor electronic device. The present invention also has for a second object to provide a method that enables making an n-type semiconductor diamond which having a perfect crystallinity is applicable to a semiconductor device.

#### 15 Summary of the Invention

In order to achieve the first object mentioned above, there is provided in accordance with the present invention, an n-type semiconductor diamond, characterized by: a crystalline perfectness whereby: it has impurity atoms constituted by sulfur atoms forming a single donor level of 0.38 eV, it has a carrier mobility's temperature dependency which at a temperature (T) range in excess of the room temperature is  $T^{-3/2}$  dependent, and it has a diamond peak in its Raman spectrum, whose half-breadth is  $2.6 \text{ cm}^{-1}$ ; a crystalline perfectness whereby: a light emission by excitons is observable; and a crystalline perfectness whereby: a distinct Kikuchi pattern in its reflection electron diffraction analysis is observable.

An n-type semiconductor diamond according to the present invention is made preferably to have at a room temperature, a carrier concentration not less than  $1.4 \times 10^{13} \text{ cm}^{-3}$  and a carrier mobility not less than  $580 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ .

An n-type semiconductor diamond according to the present invention that is thus high in crystalline perfectness can be combined

with the conventional p-type semiconductor diamond fabrication technique to form an pn-junction having excellent electrical characteristics and is applicable to making a semiconductor electronic device.

5           In order to achieve the second object mentioned above, the present invention also provides a method of making an n-type semiconductor diamond, characterized in that it comprises: mechanically polishing a diamond substrate to make it an inclined substrate; subjecting a surface of the inclined substrate to a  
10   smoothing treatment to make it even; exciting a raw material gas made of a volatile hydrocarbon compound, a sulfur compound and a hydrogen gas by a microwave plasma while maintaining at a given temperature the substrate surface smoothed as aforesaid to cause n-type semiconductor diamond to grow epitaxially on the surface  
15   smoothed diamond substrate.

For the diamond substrate, use is made of a diamond (100) face oriented substrate.

Also, the inclined substrate surface is preferably formed to consist of steps each in the order of an atomic layer, by mechanically  
20   polishing a diamond (100) face oriented substrate so that its face normal is inclined at an angle in a range between 1.5 degree and 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made by either its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions.

Also, the hydrogen plasma exposure treatment preferably  
25   comprises a treatment of exposing the inclined substrate to the hydrogen plasma of a hydrogen pressure of 10 to 50 Torr and a microwave output of 200 to 1200 W at a substrate temperature of 700 to 1200°C for a period of 0.5 hour to 5 hours, thereby to make even the substrate surface in the order of an atomic layer.

30           Such a treatment as mentioned above makes even the substrate surface in an atomic order and gives rise to a surface having (100) faces laid stepped consecutively each in the order of an atomic layer.

Also, the aforementioned given substrate temperature lies preferably in a range between 700 and 1100°C, more preferably at 830°C.

5 A method of making an n-type semiconductor diamond according to the present invention as described above enables making an n-type semiconductor diamond that has the n-type electrical conductivity, the characteristic found where carriers are supplied from a single donor level and that is large in mobility and has crystal defect reduced to minimum.

10

#### Brief Description of the Drawings

The present invention will better be understood from the following detailed description and the drawings attached hereto showing certain illustrative forms of embodiment of the present invention. In this connection, it should be noted that such forms of embodiment illustrated in the accompanying drawings hereof are intended in no way to limit the present invention but to facilitate an explanation and understanding thereof.

In the drawings:

20 With respect to the second object:

Fig. 1 is a diagrammatic view showing a substrate surface state according to a substrate pre-treatment in an embodiment illustrated;

25 Fig. 2 is an inter-atomic force microscopic (AMF) photograph of a substrate surface configuration according to the substrate pre-treatment in the embodiment illustrated;

Fig. 3 is an optical-microscopic photograph comparing the surface configuration of a substrate subjected, and that of a substrate not subjected, to the substrate pre-treatment in the embodiment illustrated, each of which substrates thereafter had an epitaxial growth of sulfur-doped, n-type semiconductor diamond thereon;

30

Fig. 4 is a schematic view of a microwave plasma CVD apparatus used in the embodiment illustrated; and

Fig. 5 is a listing showing conditions for sulfur-doped, n-type semiconductor diamond epitaxial growth in the embodiment illustrated; With respect to the first object:

Fig. 6 is a diagram showing the temperature dependency of the

11

carrier concentration of sulfur-doped, n-type semiconductor diamond in the embodiment illustrated;

Fig. 7 is a diagram showing the temperature dependency of the mobility according to measurement of the Hall coefficient, of sulfur-doped, n-type semiconductor diamond in the embodiment illustrated;

Fig. 8 is a diagram showing the temperature dependency of the mobility according to measurement of the Hall coefficient, of sulfur-doped, n-type semiconductor diamond caused to grow at a substrate temperature of 780°C in the embodiment illustrated;

Fig. 9 is a diagram comparing the mobility of sulfur-doped, n-type semiconductor diamond of the embodiment illustrated and that of a conventional phosphorus doped, n-type semiconductor diamond;

Fig. 10 is a diagram showing the Raman spectrum of sulfur-doped, n-type semiconductor diamond of the embodiment illustrated;

Fig. 11 is a diagram showing the spectrum of a light emission by free excitons and that by bound excitons in sulfur-doped, n-type semiconductor diamond;

Fig. 12 is a diagram showing crystallinity of sulfur-doped, n-type semiconductor diamond of the embodiment illustrated where (a) shows a secondary electron microscopic (SEM) image and (b) shows a reflection electron diffraction (RHEED) pattern; and

Fig. 13 is a diagram showing an atomic percentage profile on secondary ion mass spectrometric analysis of sulfur-doped, n-type semiconductor diamond of the embodiment illustrated.

#### Best Modes for Carrying Out the Invention

Hereinafter, the present invention for a method of making an n-type semiconductor diamond will be described in detail with respect to suitable embodiments thereof with reference to the drawing figures.

It should be noted at this point that while any form of energy, electrical, thermal and light may here be used singly or in

combination depending on how to activate a raw material or feed stock gas to cause an n-type semiconductor diamond to grow, the embodiment being illustrated makes use of an epitaxial growth process by a microwave plasma CVD (chemical vapor deposition, or  
5 chemical gas phase growth) that utilizes electrical and thermal energies.

Mention is first made of a preliminary treatment for a substrate on which to have epitaxial growth in the embodiment illustrated. This substrate pre-treatment is featured by (1)  
10 mechanically polishing a diamond (100) face oriented substrate surface so that its face normal is inclined at an angle of 1.5 to 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made either by its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or by its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions to form an inclined substrate, (2) surface smoothening this  
15 inclined substrate by exposing it to hydrogen plasma.

(1) Mechanical polishing is carried out using diamond grains of a grain size of not greater than  $0.5 \mu\text{m}$ .

(2) Surface smoothening makes use of a microwave plasma apparatus to be described in detail later and operating at a microwave  
20 output of 200 to 1200 W at 2.45 GHz and is performed under a hydrogen pressure of 10 to 50 Torr for a period of 0.5 to 5 hours for the substrate heated at a substrate temperature of 700 to 1200°C. Alternatively, the surface smoothening treatment may be performed by exposure to an oxygen flame such as acetylene combustion flame.

25 Fig. 1(a) shows in a diagrammatic cross sectional view a state of the substrate prepared by the above-mentioned pre-treatment, viz. by mechanical polishing effected such that the face normal of the diamond (100) substrate is inclined at any angle in a range between 1.5 and 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane  
30 formed either by its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or by its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions.

As shown in the Figure, the inclined substrate surface has its

enveloping surface with an angle ( $\alpha$ ) of inclination established at the time of mechanical polishing but as seen microscopically is extremely uneven and irregular in atomic order in size.

Fig. 1(b) shows in like diagrammatic cross sectional view a state of the substrate surface after it has been smoothened by the surface smoothening treatment, e.g., by exposing the inclined substrate's surface to hydrogen plasma as mentioned above.

As shown in this Figure, the substrate surface is smoothened in atomic order in size by the surface smoothening treatment and also seen to have the (100) faces lying stepped in the order of atomic layers in size.

Fig. 2(a) is a photograph by an interatomic force microscope (AFM) of the surface of the inclined substrate prepared by mechanical polishing (1) above.

From this Figure, it is seen that in the surface there exist a large number of differences in level and also a large number of fine and elongate scored traces made by the abrasive particles used in the mechanical polishing.

To wit, the surface of an inclined substrate prepared by the step (1) above has its envelope surface with a given angle of inclination established at the time of mechanical polishing as shown in Fig. 1(a) but as seen microscopically is very much uneven and irregular in the atomic order in size.

Fig. 2(b) is a photograph by AFM of this inclined substrate's surface after it had the smoothening treatment (2) mentioned above.

From this Figure, it is found that the differences in level seen in Fig. 2(a) are lessened and the fine and elongate scored traces by the abrasive grains are eliminated as well so that the surface is made even on an atomic level in size. To wit, the surface smoothening treatment (2) as diagrammatically shown in Fig. 1(b) does smoothen the substrate surface in the atomic order and also change the (100) face to make such face surfaces lying stepped consecutively each in

the order of an atomic layer.

Fig. 3(a) is a photograph by the optical microscope of the surface of a thin film of n-type diamond of a thickness of  $1\mu\text{m}$  caused to grow epitaxially by the microwave plasma CVD process on the substrate pre-treated (1) and (2) as above, the process using a raw material gas containing methane and hydrogen sulfide gases diluted with hydrogen gas. Fig. 3(b) is an optical microscopic photograph of an n-type diamond thin film made to epitaxially grow (under the same conditions as above) on the (100) substrate neither (1) nor (2) pre-treated.

As is seen in Fig. 3(a), it is found that a substrate pre-treated by (1) and (2) above allows a thin film of n-type diamond to epitaxially grow thereon whose surface is flat and even in the atomic order. This n-type diamond thin film also exhibits an extremely high perfect crystallinity on measurements that assess crystallinity, e.g., measurement of the temperature dependency of the mobility, observation of light emission by excitons and measurement of the Raman spectrum, as will be described later in greater details.

On the other hand, as is seen in Fig. 3(b), it is found that the surface of an n-type diamond thin film made to epitaxially on a substrate omitting pre-treatments (1) and (2) above has growth of a triangular twin crystal and is also quite irregular reflecting the polishing traces. Further, this n-type diamond thin film shows, also on measurements that assess crystallinity, no such high perfect crystallinity as with the n-type diamond thin film made to grow on the substrate pre-treated by (1) and (2) above.

Further, a substrate if mechanically polished parallel to the (100) face, i.e., at an angle of inclination of 0 degree, and its surface is then smoothened in pre-treatment (2), gives rise to a surface having (100) faces, each in the order of an atomic layer, connected one to the next rising and sinking alternately as shown in Fig. 1(c). If such a surface is used to allow an n-type diamond thin film to

epitaxially grow thereon, then the resulting surface of the thin film will have a twin crystal developed as shown Fig. 3(b) and will also yield no favorable result as in the above-mentioned measurements assessing its crystallinity.

5 As explained above, the pre-treatment of a substrate for epitaxial growth thereon by (1) mechanically polishing the substrate so that its diamond (100) face normal is inclined at an angle of 1.5 to 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made either by its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or by its  $\langle 100 \rangle$  and  $\langle 001 \rangle$   
10 directions to form an inclined substrate, and then (2) exposing this inclined substrate to a hydrogen plasma to smoothen the substrate surface permits the substrate surface to be made even in an atomic order and to lie stepped contiguously each in the order of an atomic layer. And, when made to epitaxially grow on this substrate, an n-  
15 type diamond is allowed to form thereon that is excellent in crystallinity.

An explanation is next given in respect of the microwave plasma CVD apparatus used in the embodiment illustrated. Fig. 4 is a diagrammatic view of the microwave plasma CVD apparatus used in  
20 the embodiment illustrated.

Referring to Fig. 4, the microwave plasma CVD apparatus used in the embodiment illustrated includes a microwave generating means 1, for example, of 2.45 GHz, a combined isolator and power monitor 3, a tubular reaction vessel 7 that is provided with a tuner 5  
25 and in use is irradiated with microwaves, and a vacuum pump (not shown) for evacuating the tubular reaction vessel 7 to vacuum. Also included in the microwave plasma CVD apparatus are a gas supply line 9 for supplying the tubular reaction vessel 7 switchably with a mixture gas as the raw material gas and a purging gas. The apparatus  
30 further includes a plurality of optical windows 11, 11, a substrate holder 13 mounted in the tubular reaction vessel, and a temperature control system 17 for heating or cooling a substrate 15 mounted on

the substrate holder 13. With the gas supplied onto substrate 15, the apparatus is designed to produce a microwave plasma. Also, an optical pyrometer is used to monitor the substrate temperature.

5 An explanation is next given in respect of conditions for the epitaxial growth of an n-type semiconductor diamond by the microwave plasma CVD process. These growth conditions vary depending on raw material, temperature, pressure, the gas rate of flow, the added amount of an impurity and substrate area and so on.

10 Fig. 5 is a listing of the homo-epitaxial growth conditions for sulfur-doped semiconductor diamond in the embodiment illustrated. In the explanation referring to Fig. 5, while in the embodiment illustrated a mixture gas or gaseous mixture of methane, hydrogen sulfide and hydrogen is used for the raw material gas, use may be made of a gaseous mixture of a volatile hydrocarbon compound such  
15 as alkane or alkene, a sulfur compound gas and hydrogen as well for the raw material gas. Here, use is made of a hydrocarbon as the source of supply of carbon as the principal constituent of diamond, of a sulfur compound as the source of supply of donor atoms and of hydrogen as the source of supply of carriers.

20 While use may be made, for example, of methane, ethane or propane for the alkane, and of ethylene or propylene for the alkene, it is highly desirable that use be made of methane for the volatile hydrocarbon compound since methane can easily limit supply of carbon as the constituent of diamond to minimum.

25 For the sulfur compound, while an inorganic sulfur compound such as  $H_2S$  and  $CS_2$  and an organic sulfur compound such as lower alky mercaptan can be listed, hydrogen sulfide is most preferable.

It follows, therefore, that for the gaseous mixture, use should preferably be made of a combination of methane, hydrogen sulfide and  
30 hydrogen, desirably with a methane concentration of 0.1 to 5 %, preferably 0.5 to 3.0 %, and with a hydrogen sulfide concentration of 1 to 2000 ppm, preferably 5 to 200 ppm.

5 In the embodiment illustrated, methane has a concentration of 1 % and hydrogen sulfide has a concentration of 10 to 100 ppm. For the carrier concentration that increases as the hydrogen sulfide concentration is increased, 50 ppm is most preferably because in the above range of concentration of hydrogen sulfide the mobility becomes maximum when hydrogen sulfide is added in an amount of 50 ppm.

10 For the total gas rate of flow that depends on the scale of the apparatus such as the volume of the tubular reaction vessel, the flow rate of the gasses supplied and the volume of the exhaust gases, use is made of 200 ml/min in this embodiment.

15 While the gas rates of flows are controlled using a mass flow controller so as to correspond to the particular gas kinds, the amount of addition of hydrogen sulfide is established, for example, by using a mixture gas bomb containing 100 ppm of hydrogen sulfide and the balance hydrogen, diluting the mixture gas with carrier hydrogen and having the mass flow controller control the resultant gaseous mixture so that a predetermined proportion of the addition is attained.

20 In the embodiment illustrated, use is made of a mixture gas bomb containing 100 ppm of hydrogen sulfide and the balance hydrogen. In the embodiment illustrated where the concentration of hydrogen sulfide is set at 50 ppm, if the total rate of flow is set at 200 ml/min, flow of the carrier hydrogen gas at a rate of 100 ml/min by  
25 flow of the mixture gas of 100 ppm hydrogen sulfide and the balance hydrogen permits establishing the concentration of hydrogen sulfide at 50 ppm in the aggregate.

30 For the atmospheric pressure that generally lies in the range of 30 to 60 Torr in the microwave plasma CVD process, use is made of 40 Torr in the embodiment illustrated. The microwave discharge is a glow discharge that can be sustained at a relatively high pressure.

For the temperature at which to precipitate diamond that

generally ranges between 700 and 1100°C, use is made of 830°C in the embodiment illustrated.

While use is made here of Ib diamond for the diamond that makes up the substrate, the diamond is not limited to this type but may be of Ia or II type. Also, while the face having homo-epitaxial diamond growth produced thereon is the (100) face in the embodiment illustrated, it is not limited so but may, for example, be the (111) or (110) face as well.

An explanation is next given in respect of a process of making an n-type semiconductor diamond in accordance with the embodiment illustrated.

First in the pre-treatment of a substrate for having epitaxial growth thereon, diamond grains of a grain size not more than  $0.5\mu$  m are used to give the substrate an inclined surface by mechanical polishing. In so doing, a diamond (100) face oriented substrate surface is mechanically polished so that its face normal is inclined at an angle in a range between 1.5 degree and 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made by either its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions. The inclined surface so given of the substrate (inclined substrate) is made even using the aforementioned microwave plasma apparatus by exposing therein this substrate surface to a hydrogen plasma of 2.45 GHz having a microwave output of 200 to 1200 W under a hydrogen pressure of 10 to 50 Torr with a substrate temperature of 700 to 1200°C for a treatment period of 0.5 to 5 hours. It should be noted here that such surface smoothening treatment can also be performed by exposure to an oxidizing flame such as an acetylene combustion flame as well.

The surface smoothened substrate, then after treatment by cleaning, is mounted on a substrate holder in the vacuum vessel, and nitrogen and oxygen therein is removed by repeating a hydrogen purging from the gas supply line several times. This is followed by heating the substrate holder to raise its surface temperature

controllably to 830°C and controlling its atmospheric pressure at 40 Torr. Here, the surface temperature of the substrate is measured by an optical pyrometer.

The gas supply line is then switched to replace the purging  
5 hydrogen gas with a gas mixture of 1 % methane and 50 ppm hydrogen sulfide diluted with hydrogen, which is introduced into the tubular reaction vessel to flow therethrough at a flow rate of 200 ml/min. Then, effecting a microwave discharge under the controlled atmospheric pressure of 40 Torr brings about a plasma above the  
10 substrate. With a plasma flow supplied onto the substrate, a thin diamond film is allowed to epitaxially grow thereon.

When the film has grown to a given thickness, the gas supply line is switched to resume the hydrogen purging position, the microwave discharge is terminated, and the substrate is refrained  
15 from heating or cooled.

After returning to the room temperature, the diamond substrate is taken out of the substrate holder in the tubular reaction vessel that has returned to its normal pressure.

A thin diamond film so made and formed with electrodes has  
20 been found to show the ohmic characteristic at measuring temperatures of 250 to 550 K.

All the diamond crystal film specimens so made have been found to show a negative Hall coefficient at measuring temperatures of 250 to 550 K and a mobility at the room temperature that is as high  
25 as 580 cm<sup>2</sup>/Vs. They also have a clear Kikuchi pattern observed in their reflection electron diffraction (RHEED), showing that the crystalline films are extremely high in crystallinity.

As is apparent from the above explanation, the n-type semiconductor diamond making method of the embodiment illustrated  
30 permits providing an n-type semiconductor diamond that is high in mobility and excellent in crystallinity.

An explanation is next given in respect of properties of an n-

type semiconductor diamond made in the manner described.

Fig. 6 is a diagram showing the temperature dependency of the carrier concentration of an n-type semiconductor diamond according to the embodiment illustrated.

5 As can be seen from Fig. 6, the diamond has the carrier concentration that increases from  $10^{12}$  to  $10^{16}$   $\text{cm}^{-3}$  as the temperature is raised, and has an electrical conductivity of  $1.3 \times 10^{-3} \Omega^{-1}\text{cm}^{-1}$ . It can be seen from this Figure that the carrier density is wholly exponentially dependent on the inverse of the temperature,  
10 thus indicating that the carriers are furnished from only a single donor level. It is found from Fig. 6 that an activation energy of the donor level is 0.38 eV.

It follows, therefore, that the present invention provides an n-type semiconductor diamond in which sulfur (S) atoms form a  
15 single donor level with the activation energy of 0.38 eV.

Fig. 7 is a diagram showing the temperature dependency of the mobility derived from measurement of the Hall coefficient of an n-type semiconductor diamond in the embodiment illustrated.

All the specimens have been found to show a negative Hall  
20 coefficient at measuring temperatures of 250 to 550 K.

As is apparent from Fig. 7, the carrier concentration and the mobility at the room temperature are  $1.4 \times 10^{13} \text{ cm}^{-3}$  and  $580 \text{ cm}^2/\text{Vs}$ , respectively.

Fig. 8 is a diagram showing the temperature dependency of the  
25 mobility derived from measurement of the Hall coefficient of an n-type semiconductor diamond that is grown at a temperature of  $780^\circ\text{C}$ . While the growth conditions are the same except the substrate temperature, the lower temperature reduces the amount of introduction of sulfur into the crystal.

30 In the example shown in Fig. 8, it is shown that the mobility is still as high as  $980 \text{ cm}^2/\text{Vs}$ , indicating that the n-type semiconductor diamond of the present invention is considerably high in mobility

even with the doped amount of sulfur so reduced.

It should be noted further that while the electron mobility of the Ila-type diamond is estimated to be about  $2000 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ , the method of manufacture according to the embodiment illustrated that permits preparations with excellent crystallinity permits the use of a substrate composed of the Ila-type diamond to achieve a mobility even in the order of  $10^3 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ .

Fig. 9 is a diagram that compares the sulfur (S) doped n-type semiconductor diamond with the conventional phosphorus (P) doped n-type semiconductor diamond with respect to the mobility.

In Fig. 9, data marked by  $\square$  and  $\bigcirc$  are of S-doped n-type semiconductor diamond specimens produced in accordance with the present invention at the growth temperatures of  $780^\circ\text{C}$  and  $830^\circ\text{C}$ , respectively; and data indicated by the dark circle and the chain line are of the conventional P-doped n-type semiconductor diamond.

The conventional P-doped semiconductor diamond has a mobility at the room temperature in the order of  $10 \text{ cm}^2/\text{Vs}$  and  $30 \text{ cm}^2/\text{Vs}$  at the highest. See Diamond and Related Materials 7 (1998) 540 – 544, S. Koizumi et al.

In comparison, an S-doped n-type semiconductor diamond of the present embodiment illustrated as mentioned above has a mobility as high as about  $600 \text{ cm}^2/\text{Vs}$ , and thus exhibits a extremely high mobility.

Furthermore, an S-doped n-type semiconductor diamond as made by a method of making according to the present invention exhibits the mobility that is  $T^{-3/2}$ -dependent, as opposed to the temperature dependency, the temperature characteristic of the mobility found where the crystal defects are abundant.

This temperature characteristic indicates that for the carrier scattering process, what are due to phonons is controlling, and is observable only with a single crystal that is high in crystallinity perfectness. It is seen, therefore, that the source of the carriers is

not such as crystal defects but is the dopant atoms, demonstrating that the diamond has a perfect crystallinity usable for a semiconductor device.

Fig. 10 is a diagram showing the Raman spectrum of an n-type semiconductor diamond of the present embodiment. As is apparent from Fig. 10, there is seen no peak other than the peak at the wave number of  $1333\text{ cm}^{-1}$ , whose half width is as very narrow as about  $2.6\text{ cm}^{-1}$ . It is thus shown that the n-type semiconductor diamond of the present invention is very high in crystallinity. Although not shown, the wave number of an n-type semiconductor diamond produced by epitaxial growth on the substrate without having the pre-treatment is about  $6\text{ cm}^{-1}$ .

Fig. 11 is a diagram showing the spectrum of a light emission by free excitons and that of a light emission by bound excitons. For the n-type semiconductor diamond, it is apparent from Fig. 11 that the free exciton light emission (FE) and the bound exciton light emission (BE) are observed at 235 nm and 238 nm, respectively. This indicates, among others, that the doped ions are present at lattice points of the diamond lattice and moreover form a perfect donor level within the band gap thereof. On the other hand, for the n-type semiconductor diamond formed by epitaxial growth on the substrate not subjected to the pre-treatment is observed neither free exciton light emission nor bound exciton light emission.

Fig. 12(a) and 12(b) show a secondary electron microscopic (SEM) photograph and a reflection electron diffraction (RHEED) pattern, respectively. As is apparent from Fig. 12(a), the n-type semiconductor diamond in the present embodiment has an extremely smooth surface and as is so from Fig. 12(b), it exhibits a highly distinct and clear Kikuchi pattern, indicating that the same is extremely high in crystallinity.

Fig. 13 is a diagram showing a profile of the atomic percentages of the n-type semiconductor diamond in the present

embodiment, derived from secondary ion mass spectrometric analysis (SIMS). It can be seen from Fig. 13 that the n-type semiconductor diamond is doped with sulfur (S) at a constant percentage, obviously with the sulfur (S) atoms at a concentration at least more than  $10^{13}$  cm<sup>-3</sup> as a limit of detection by SIMS.

From the foregoing description, it is evident that an n-type semiconductor diamond made by the embodiment illustrated possesses the n-type conductivity, the characteristic found where carriers are supplied from a single donor level, and has few crystal defects and a large mobility. Accordingly, it can well be combined with the conventional p-type semiconductor preparation technique to provide a pn-junction that is excellent in properties.

Although the present invention has hereinbefore been set forth with respect to certain illustrative forms of embodiments thereof, it will readily be appreciated to be obvious to a person skilled in the art that many alternations thereof, omissions therefrom and additions thereto can be made without departing from the essences of scope of the present invention. Accordingly, it should be understood that the invention is not intended to be limited to the specific forms of embodiment thereof set forth below, but to include all possible forms of embodiment thereof that can be made within the scope with respect to the features specifically set forth in the appended claims and encompasses all the equivalents thereof.

#### Industrial Applicability

As has been set forth in the foregoing description, an n-type semiconductor diamond and a method of making the same according to the present invention make possible industrial manufacture of semiconductor electronic devices by semiconductor diamond thin films, and make it possible to realize manufacture of such devices as a small, high power device, a high output, high frequency device and a high temperature operating device.

What is claimed is:

1. (As amended) An n-type semiconductor diamond, characterized by:

5 a crystalline perfectness whereby:

it has impurity atoms constituted by sulfur atoms forming a single donor level of 0.38 eV,

10 it has a carrier mobility's temperature dependency which at a temperature (T) range in excess of the room temperature is  $T^{-3/2}$  dependent, and

it has a diamond peak in its Raman spectrum, whose half width is  $2.6 \text{ cm}^{-1}$ ;

a crystalline perfectness whereby:

light emission by excitons is observable; and

15 a crystalline perfectness whereby:

a distinct Kikuchi pattern in its reflection electron diffraction analysis is observable.

20 2. (As amended) An n-type semiconductor diamond as set forth in claim 1, characterized in that at a room temperature it has a carrier concentration not less than  $1.4 \times 10^{13} \text{ cm}^{-3}$  and

a carrier mobility not less than  $580 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$ .

25 3. (As amended) A method of making an n-type semiconductor diamond, characterized in that it comprises:

mechanically polishing a diamond substrate to make it an inclined diamond substrate;

subjecting a surface of said inclined diamond substrate to a smoothening treatment make it even;

30 exciting a raw material gas made of a volatile hydrocarbon compound, a sulfur compound and a hydrogen gas by a microwave plasma while maintaining at a given temperature said substrate

surface smoothened as aforesaid to cause n-type semiconductor diamond to grow epitaxially on said surface smoothened substrate.

4. (As amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said diamond substrate is a diamond (100) face oriented substrate.

5. (As amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said inclined substrate is formed to consist of steps each in the order of an atomic layer, by mechanically polishing a diamond (100) face oriented substrate so that its face normal is inclined at an angle in a range between 1.5 degree and 6 degrees with respect to its  $\langle 100 \rangle$  direction in a plane made by either its  $\langle 100 \rangle$  and  $\langle 010 \rangle$  directions or its  $\langle 100 \rangle$  and  $\langle 001 \rangle$  directions.

6. (As amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said hydrogen plasma exposure treatment comprises a treatment of exposing said inclined substrate to the hydrogen plasma of a hydrogen pressure of 10 to 50 Torr and a microwave output of 200 to 1200 W at a substrate temperature of 700 to 1200°C for a period of 0.5 hour to 5 hours, thereby to make even said substrate surface in the order of an atomic layer.

25

7. (As amended) A method of making an n-type semiconductor diamond as set forth in claim 3, characterized in that said given substrate temperature lies in a range between 700 and 1100°C, preferably at 830°C.

30

8.~19. (As deleted)

## Abstract

A substrate is polished and made an inclined substrate, which is exposed to a hydrogen plasma and is thereby smoothened. The substrate is then heated controlledly until its surface temperature reaches 830°C. Meanwhile, a gas mixture of 1% methane, 50 ppm hydrogen sulfide and hydrogen is introduced in a tubular reaction vessel to flow therethrough at 200 ml/min, where microwave plasma is excited to cause n-type semiconductor diamond to epitaxially grow on the substrate. An ion doped n-type semiconductor is thus formed that has a single donor level of an activation energy at 0.38 eV and is high in mobility and of high quality.

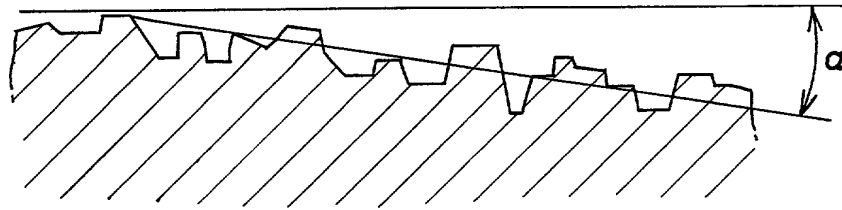


FIG. 1A

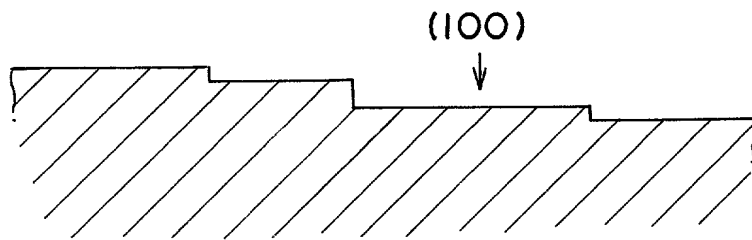


FIG. 1B

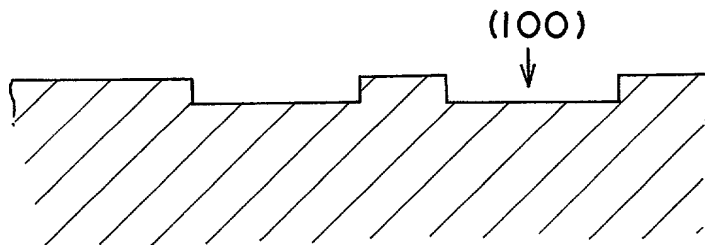


FIG. 1C

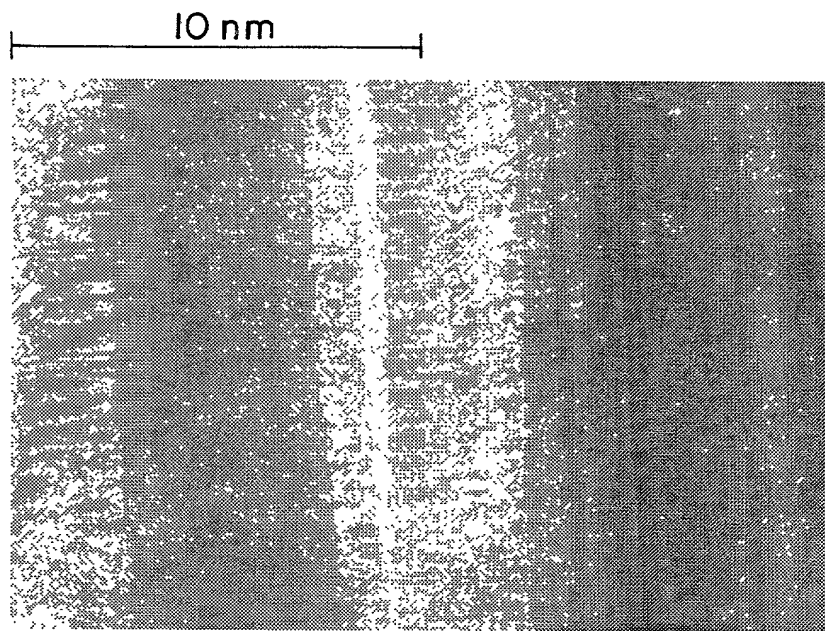


FIG. 2A

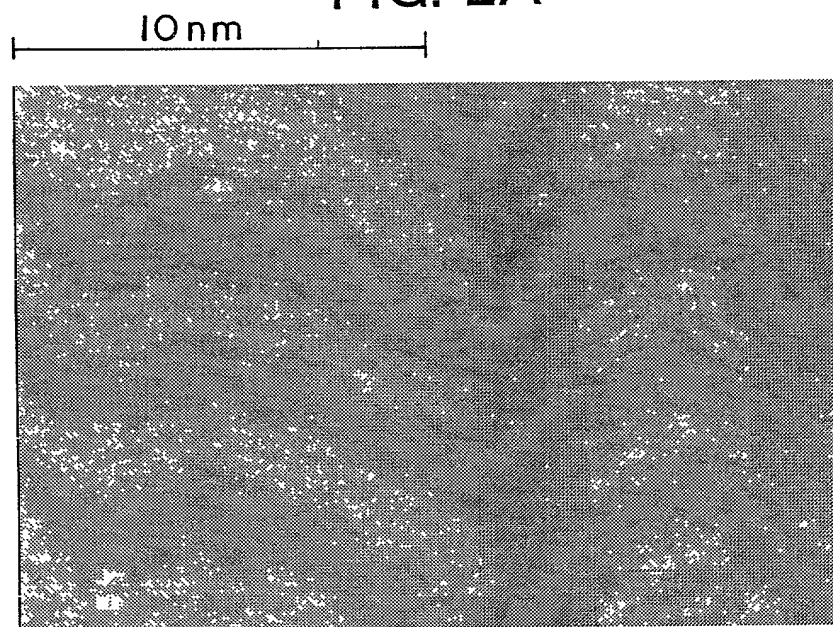


FIG. 2B

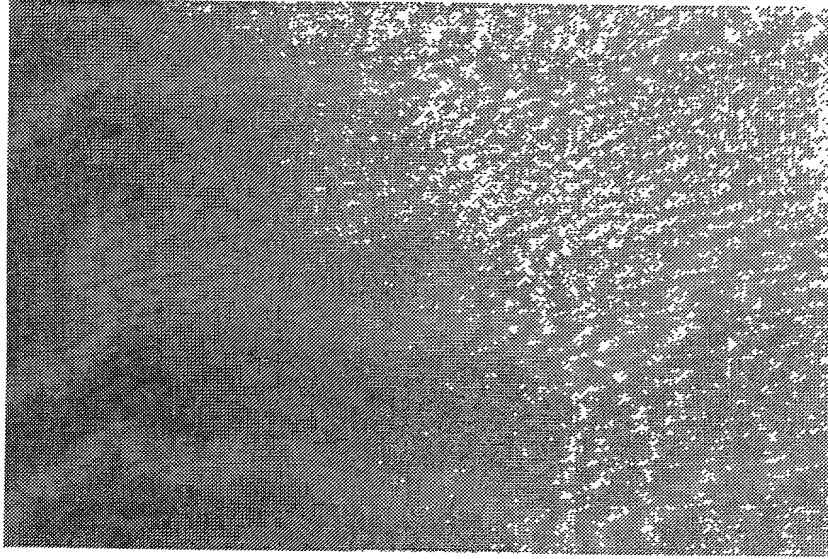
10  $\mu$ m

FIG. 3A

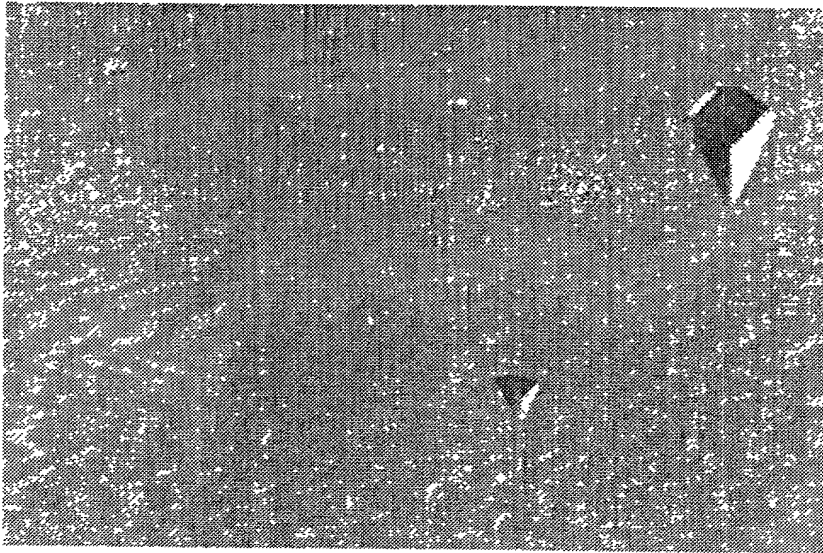
10  $\mu$ m

FIG. 3B

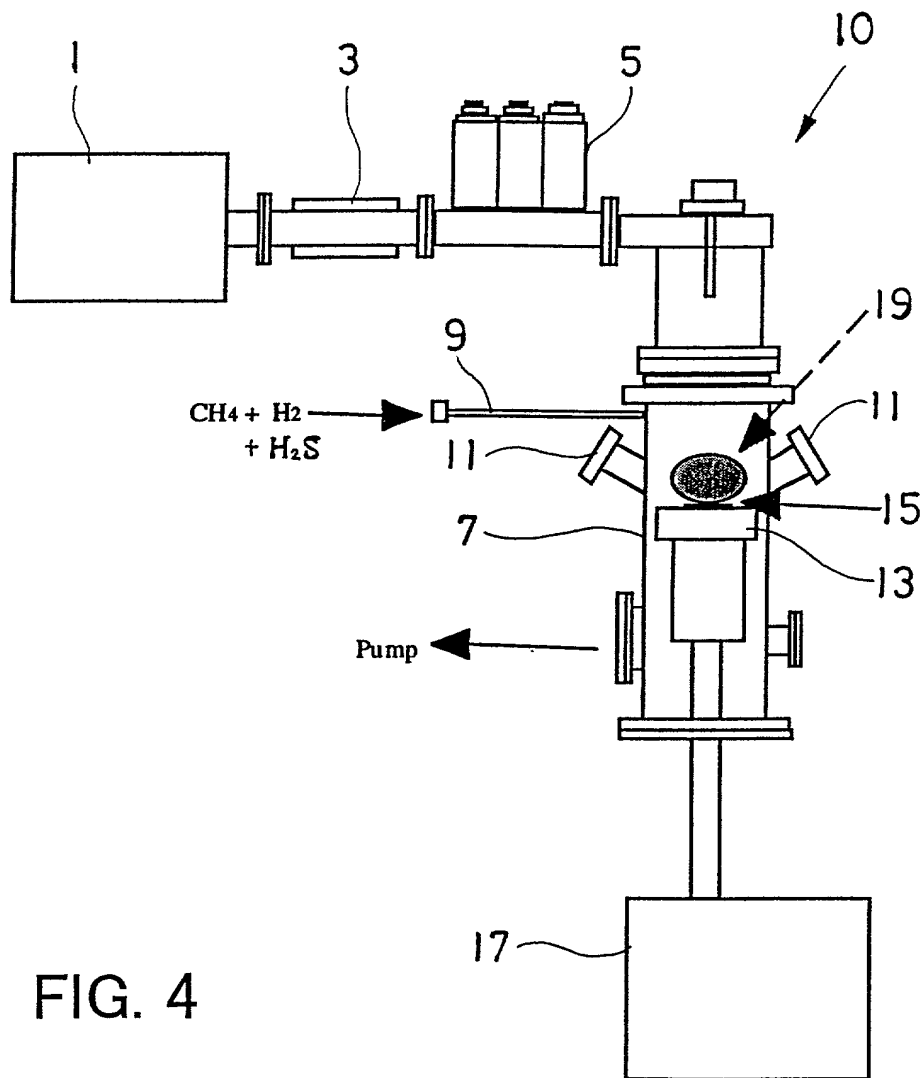


FIG. 4

Gas source	CH <sub>4</sub> /H <sub>2</sub> S/H <sub>2</sub>
CH <sub>4</sub> Concentration	1. 0%
H <sub>2</sub> S Concentration	10~100 ppm
S/C Ratio	1000~10000 ppm
Total gas flow rate	200 ml min <sup>-1</sup>
Gas pressure	40 Torr
Microwave	2. 45GHz, 300~400W
Substrate temperature	830°C
Diamond substrate	HP-HT1b Diamond (100)

FIG. 5

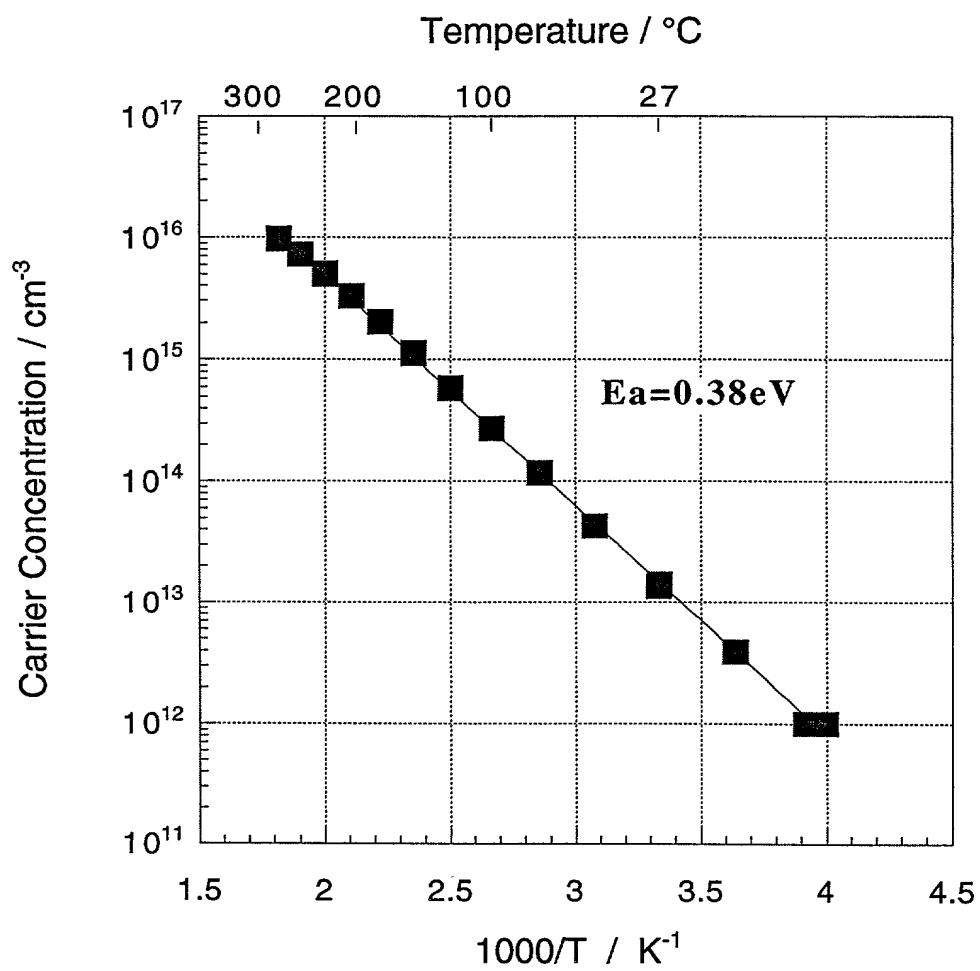


FIG. 6

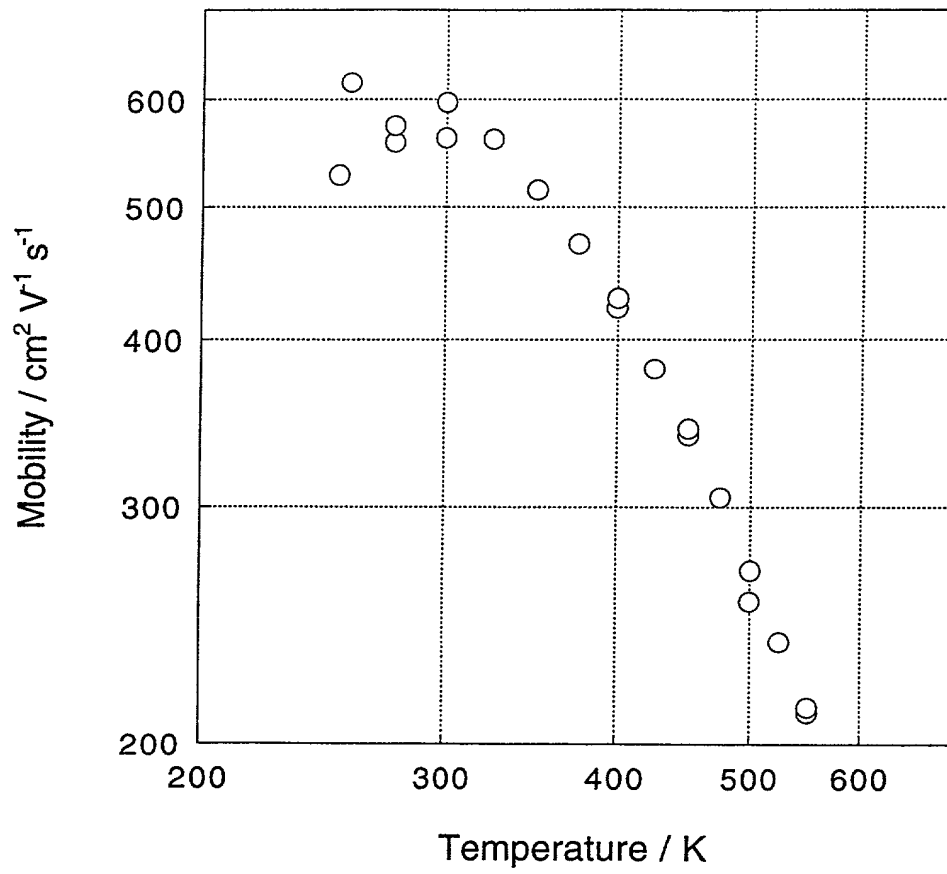


FIG. 7

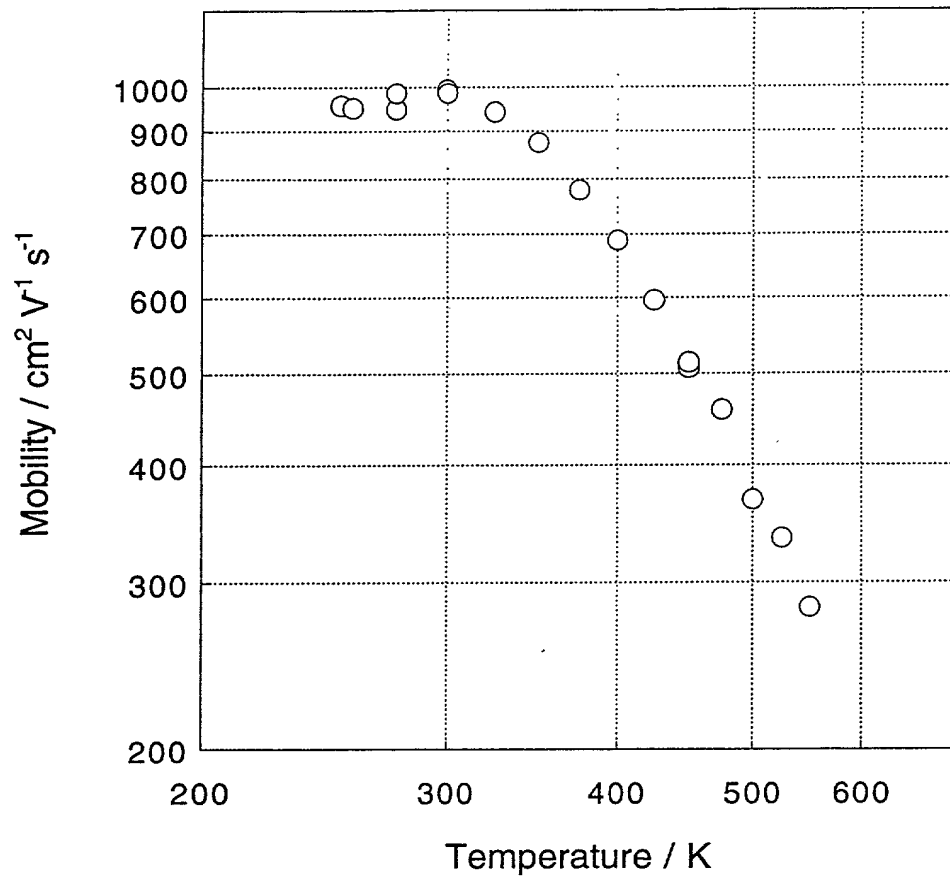


FIG. 8

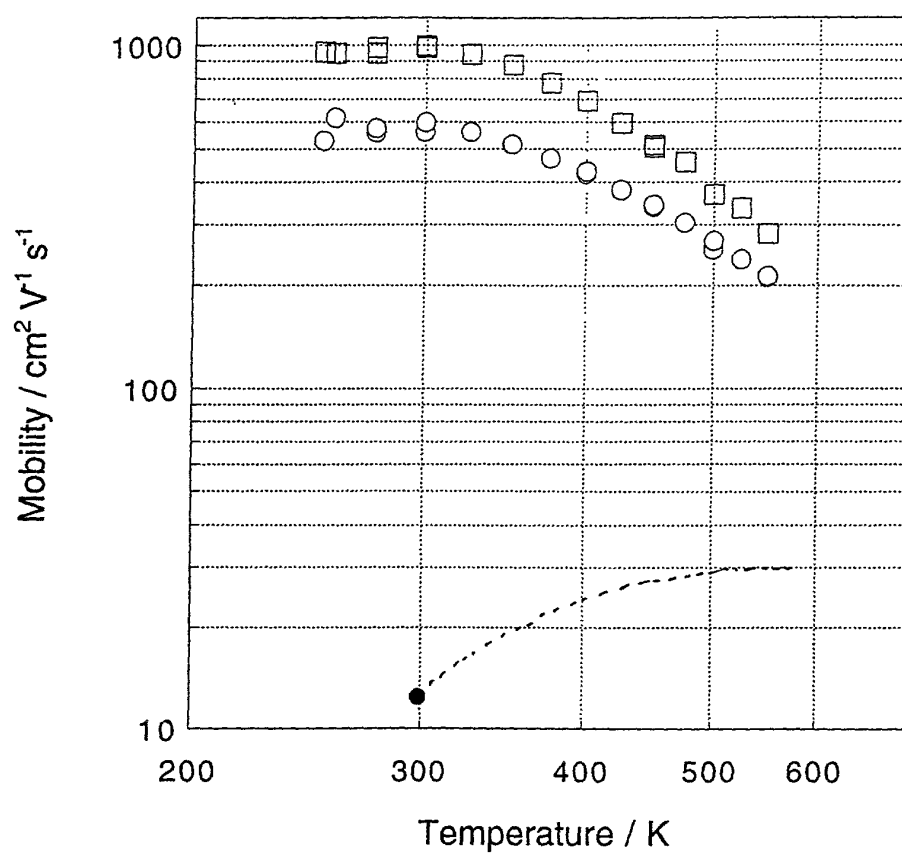


FIG. 9

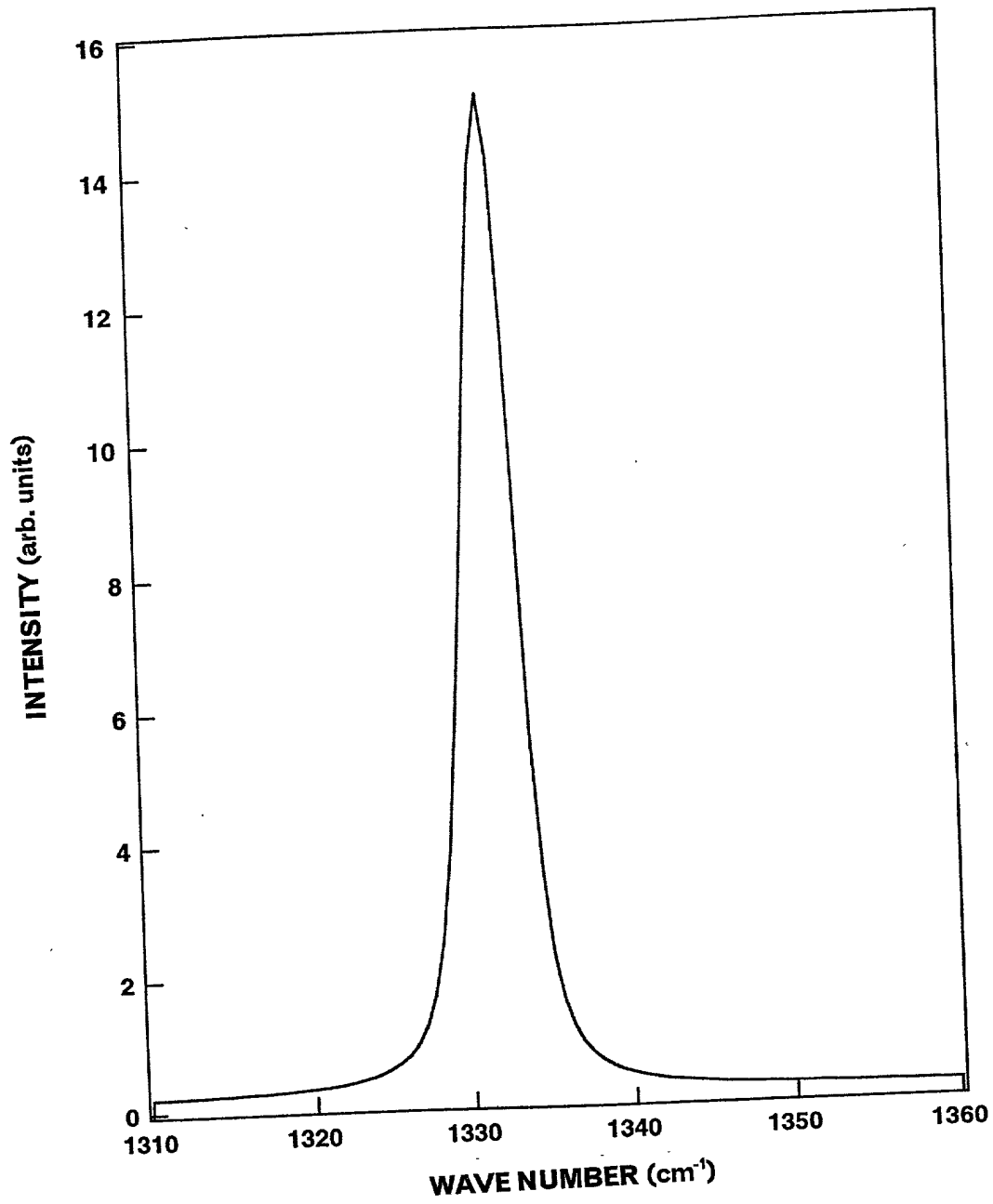


FIG. 10

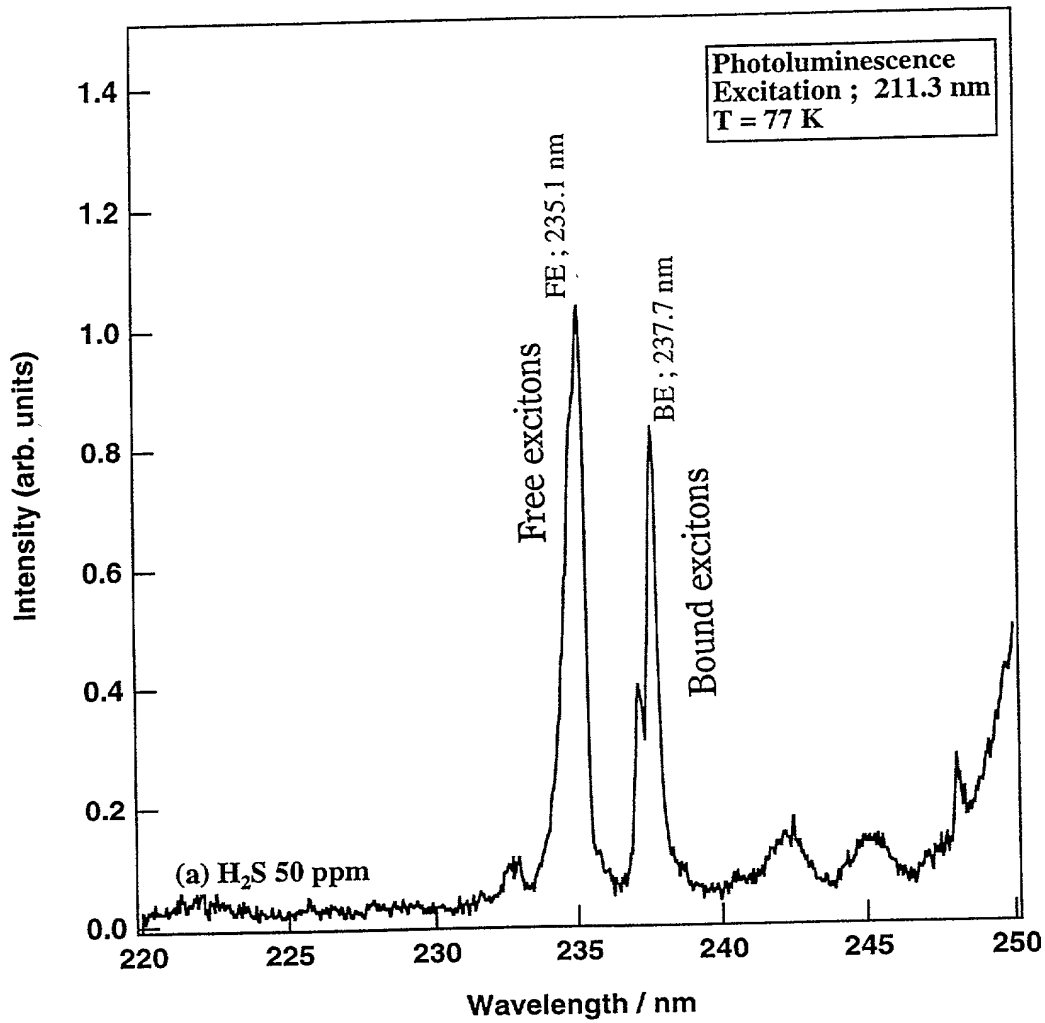


FIG. 11

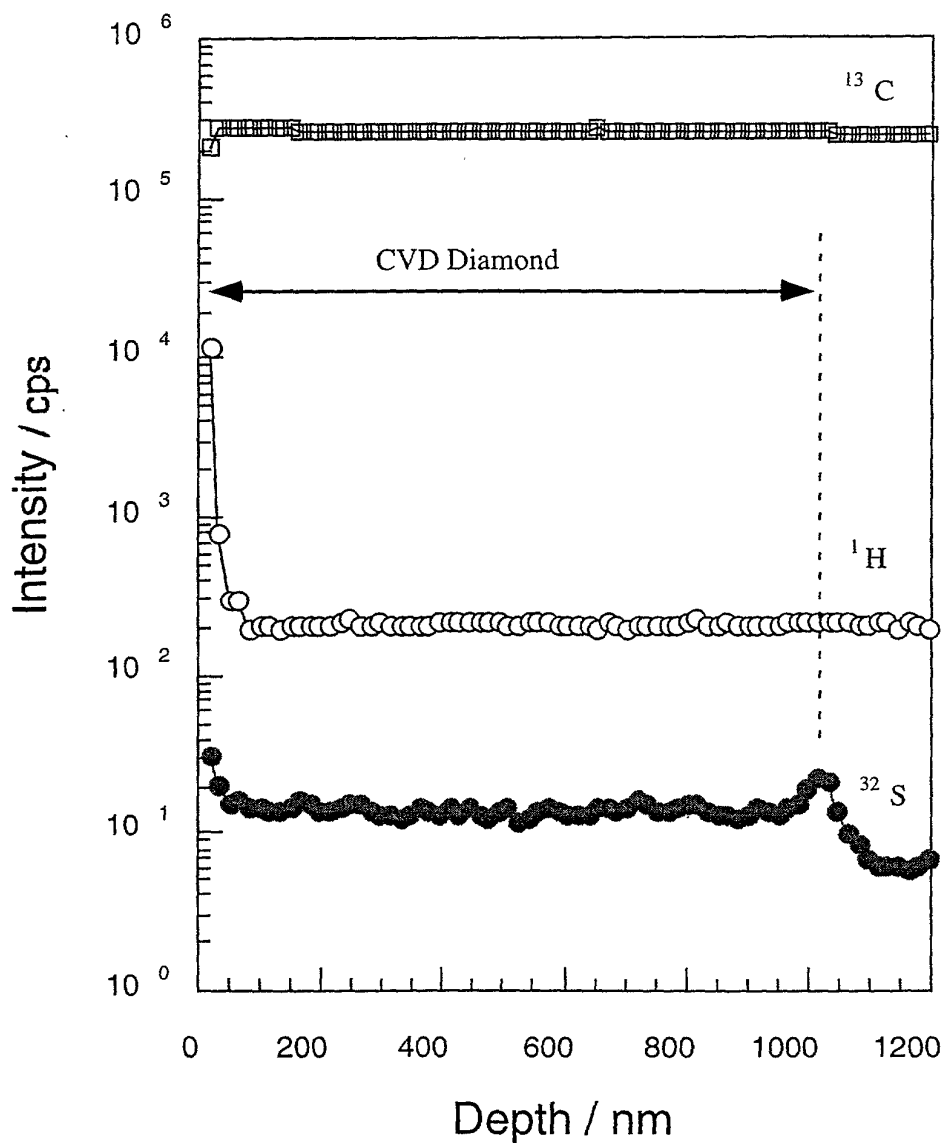


FIG. 13

## Declaration For U.S. Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention **entitled** (Insert Title) "N-TYPE SEMICONDUCTOR DIAMOND AND METHOD OF ITS MAKING"

the specification of which is attached hereto unless the following is checked:



was filed on March 27, 2000 as United States Application Number or PCT International Application Number PCT/JP00/01863 and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 (a) - (d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

(List prior foreign applications. See note A on back of this page)	1999-124682 (Number)	Japan (Country)	26/03/1999 (Day/Month/Year Filed)	Priority Claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
	(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No
	(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No
	(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No

(See note B on back of this page)

☐ See attached list for additional prior foreign applications

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below.

(Application Number)	(Filing Date)
(Application Number)	(Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of the application:

(List Prior U.S. Applications)	(Application Serial Number)	(Filing Date)	(Status) (patented, pending, abandoned)
	(Application Serial Number)	(Filing Date)	(Status) (patented, pending, abandoned)

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

James E. Armstrong, III, Reg. No. 18,366; William F. Westerman, Reg. No. 29,988; Ken-Ichi Hattori, Reg. No. 32,861; Le-Nhung McLeland, Reg. No. 31,541; Ronald F. Naughton, Reg. No. 24,616; John R. Pegan, Reg. No. 18,069; William G. Kratz, Jr., Reg. No. 22,631; Albert Tockman, Reg. No. 19,722; Mel R. Quintos, Reg. No. 31,898; Donald W. Hanson, Reg. No. 27,133; Stephen G. Adrian, Reg. No. 32,878; William L. Brooks, Reg. No. 34,129; John F. Carney, Reg. No. 20,276; Edward F. Welsh, Reg. No. 22,455; Patrick D. Muir, Reg. No. 37,403; Gay A. Spahn, Reg. No. 34,978; John P. Kong, Reg. No. 40,054; and Luke A. Kilyk, Reg. No. 33,251.

Please direct all communications to the following address:

ARMSTRONG, WESTERMAN, HATTORI,  
McLELAND & NAUGHTON  
1725 K Street, N.W., Suite 1000  
Washington, D.C. 20006  
Telephone: (202) 659-2930 Fax: (202) 887-0357

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18 of the United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

1-00  
(See note C above)  
Full name of sole or first inventor (given name, family name) Toshihiro, ANDO  
Inventor's Signature Toshihiro Ando Date 28/08/2001

Residence Ibaraki, Japan Citizenship Japan

2-00  
Post Office Address c/o National Institute for Materials Science, 2-1, Sengen 1-chome, Tsukuba-shi, Ibaraki 305-0047 Japan

Full name of second inventor (given name, family name) Yoichiro, SATO

Inventor's Signature Yoichiro Sato Date Aug. 29. 2001

Residence Ibaraki, Japan Citizenship Japan

3-00  
Post Office Address c/o National Institute for Materials Science, 2-1, Sengen 1-chome, Tsukuba-shi, Ibaraki 305-0047 Japan

Full name of third inventor (given name, family name) Eiji, YASU

Inventor's Signature Eiji Yasu Date 29/08/2001

Residence Ibaraki, Japan Citizenship Japan

4-00  
Post Office Address c/o National Institute for Materials Science, 2-1, Sengen 1-chome, Tsukuba-shi, Ibaraki 305-0047 Japan

Full name of fourth inventor (given name, family name) Mika, GAMO

Inventor's Signature 薄生美香 Date 28/08/2001

Residence Ibaraki, Japan Citizenship Japan

5-00  
Post Office Address c/o National Institute for Materials Science, 2-1, Sengen 1-chome, Tsukuba-shi, Ibaraki 305-0047 Japan

Full name of fifth inventor (given name, family name) Isao, SAKAGUCHI

Inventor's Signature Isao Sakaguchi Date 28/08/2001

Residence Ibaraki, Japan Citizenship Japan

Post Office Address c/o National Institute for Materials Science, 2-1, Sengen 1-chome, Tsukuba-shi, Ibaraki 305-0047 Japan

Full name of sixth inventor (given name, family name) \_\_\_\_\_

Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

Residence \_\_\_\_\_ Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_

Full name of seventh inventor (given name, family name) \_\_\_\_\_

Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

Residence \_\_\_\_\_ Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_

Full name of eighth inventor (given name, family name) \_\_\_\_\_

Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

Residence \_\_\_\_\_ Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_